

JSN270

Ultra Low Power Consumption
Support Industrial Environment
Fast Boot and Compact size



저전력 802.11b/g/n Serial to Wi-Fi Module

JSN270은 UART 신호 및 SPI 신호를 IEEE802.11 b/g/n 무선랜 프로토콜로 변환시키는 게이트웨이 모듈로서 Serial interface를 가진 기존 장비를 무선랜 망에 연결하여 원격 측정, 관리 및 제어할 수 있게 해주는 제품입니다. Serial2WiFi Gateway Module로 Serial interface를 가진 어떤 디바이스도 손쉽게 Wi-Fi를 구현할 수 있습니다.

JSN270 제품 소개

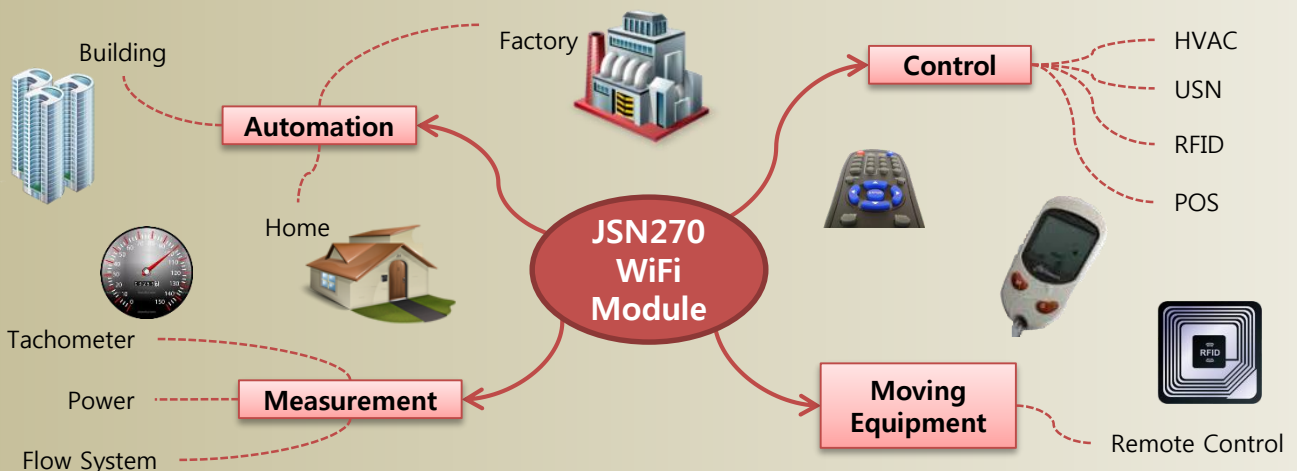
신뢰와 기술, 제이엠펜시스템

Key Features

- Embedded 802.11b/g/n Wireless Networking
- Serial to Wi-Fi Device
- Fast booting time (RTOS 기반)
- End to end TCP Socket Connection in a few seconds
- Timer and Event Triggered Auto-reporting Capability
- -40 to +85 °C Operating Temperature Range
- FCC and ETSI compliant for Unlicensed Operation
- Compact design 16.4mm X 22mm X 2.3mm
- Strong Security with 64/128 bit WEP, WPA, WPA2
- Simple AT Command for configuration
- Small Size, Light Weight, Low Cost, Easy to use
- RoHS Compliant

IoT (M2M)
WiFi Solution

Made in Korea



Aircess™ WiFi AP

Specification

제품 명칭 / 모델명 : JSN270

Wireless

Wireless Standard	IEEE 802.11 b/g/n
Frequency Range	2.412~2.477 GHz
Output Power	17dBm@1Mbps, 15dBm@11n MCS0, 13dBm@11n MCS7
Receive Sensitivity	-97dBm@1Mbps, -93dBm@11n MCS0, -75dBm@11n MCS7
Data Rates	Max 72Mbps
Modulation Type	OFDM, DSSS

Hardware

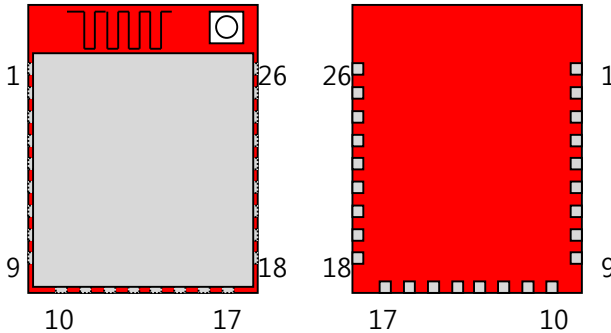
Interface	UART (3.3V TTL level, no flow control) SPI, GPIO
	내장 PCB 안테나
Temperature	Operation: -40°C~85°C
Humidity	Operation: 10% ~ 90%, Non-Condensing
Serial	UART : 1200~921600bps (default 115200bps)
	SPI supported for high speed connection (별도의 SDK 필요)
Supply Voltage	DC 3.15V~3.45V
Power Consumption	Typ. 200mA@Tx, 130mA@operation (평균)
Dimension	16.4mm X 22mm X 2.3mm

Software

Serial Command Format	AT Command Set
Operation Mode	Infra-structure, Access Point
Protocol	ARP, UDP, TCP, ICMP, DHCP server, DHCP client, HTTP
WiFi Security	WEP 64/128bit, WPA/WPA2 personal
API	TBD

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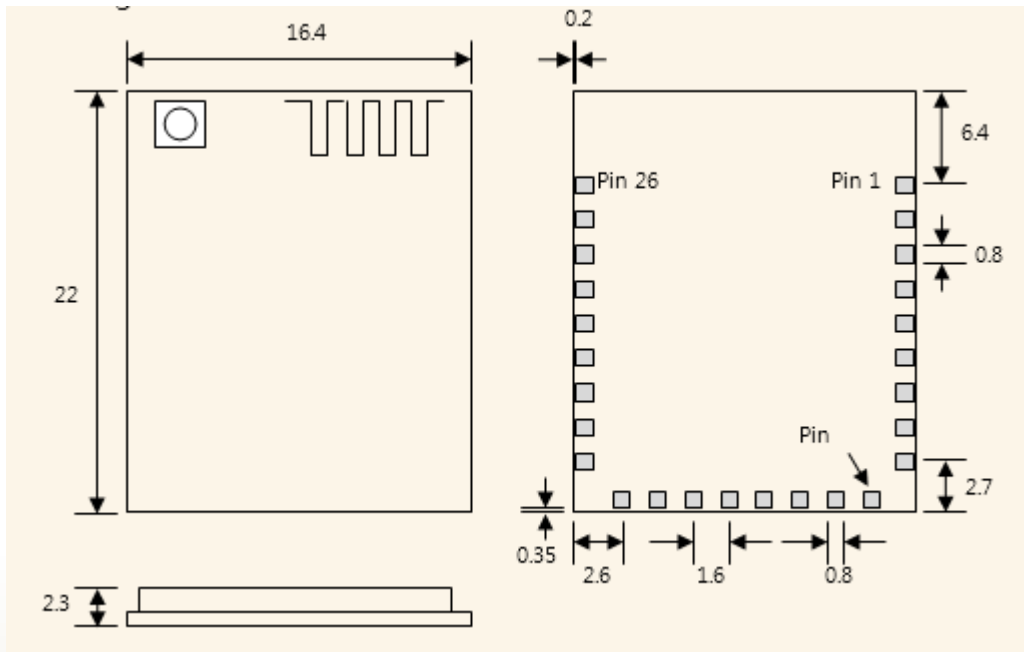
Pin Assignment



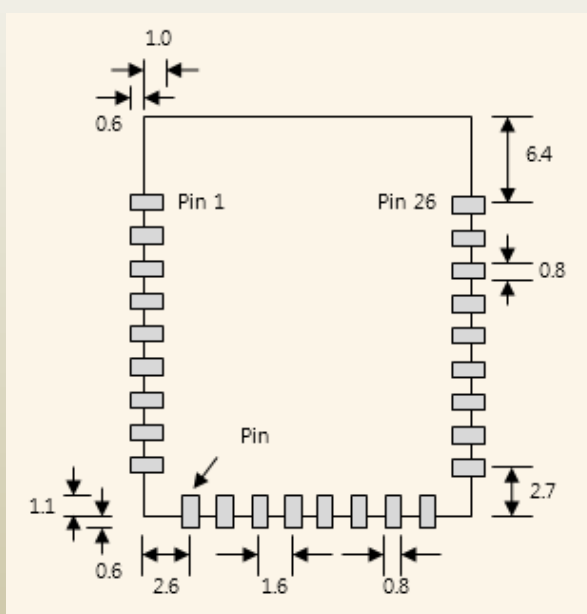
No.	Name	Direction	Description
1	RESET_N	Input	External Reset Input. Active low You may leave this pin unconnected
2	GND	Power	GND
3	SF_SEL	Input	Factory test only. Do not connect.
4	VDD_3V3	Power	3.3V
5	Reserved		Do not connect.
6	VDD_3V3	Power	3.3V
7	Reserved		Do not connect.
8	GND	Power	GND
9	GND	Power	GND
10	UART2_TX	Output	UART2 TX, 3.3V TTL level.
11	UART2_RX	Input	UART2 RX, 3.3V TTL level.
12	GPIO14	In/Out	GPIO14 Can be used UART2 TX enable output
13	GPIO15	In/Out	GPIO15
14	UART0_TX	Output	Factory test only. Do not connect.
15	UART0_RX	Input	Factory test only. Do not connect.
16	Reserved		Do not connect.
17	Reserved		Do not connect.
18	Reserved		Do not connect.
19	Reserved		Do not connect.
20	Reserved		Do not connect.
21	Reserved		Do not connect.
22	SPI_CLK	Input	SPI clock
23	SPI_MISO	Output	SPI data output
24	SPI_MOSI	Input	SPI data input
25	SPI_CS_N	Input	SPI chip select. Active low
26	PROGRAM	Input	Factory test only. Do not connect.

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Dimension

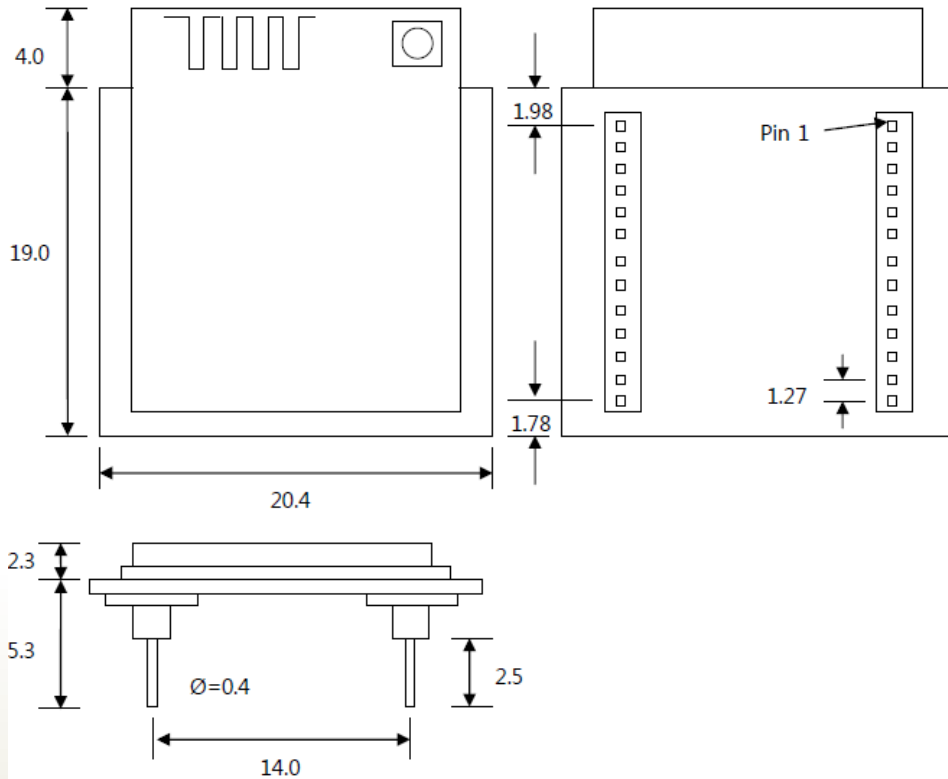


Recommended Soldering Pad



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Dimension (DIP)



Recommended PCB hole (DIP)

